Amendments to the Specification

        On page 3, after line 14 (as amended June 23, 2005), please delete the section heading:

~~DESCRIPTION OF THE PREFERED EMBODIMENTS~~

        On page 7, after line 24, please add the following new paragraphs:

BRIEF DESCRIPTION OF THE DRAWINGS

        Figure 1 is a schematic flow diagram showing a sequence used for securing a computer system according to the invention.

DETAILED DESCRIPTION OF THE PREFERED EMBODIMENT

        In this figure 1 the code to be implemented is represented by a block 1 containing a plurality of code instructions (CI_1, CI_i, ...CI_n) which have to be successively interpreted by an interpreter 2 comprising a plurality of interpreted instructions I_1, I_j, I_k and thereafter executed (bloc 3) according to an implementation.

        The interpreter 2 is provided with an implementation table 4 comprising at least one implementation $IMP_{j,1}$...$IMP_{j,t}$...$IMP_{j,m}$ for each interpreted instruction I_1... I_j... I_k.

        According to the invention for at least one interpreted instruction Ij the implementation table 4 proposes several possible implementations: implementation $IMP_{j,1}$.to.$IMP_{j,m}$.

        For this interpreted instruction I_j, the interpreter selects (bloc 5) one implementation in the table 4 (here the implementation $IMP_{j,t}$).

Once this implementation $IMP_{j,t}$ is executed (bloc 3) the system goes to the next code instruction (instruction CI_{i+1}) and starts a next sequence with this instruction CI_{i+1}.